Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of the Claims:

1.   (Previously Presented) A method comprising:

a processor executing a BIOS routine by receiving information from at least one first computing system units, the information comprises at least one of error information, status information, and configuration information;

the processor executing the BIOS routine by storing said received information in a memory;

for each of at least one second computing system units,

the processor executing the BIOS routine by receiving an initial request for said received information from the second computing system unit;

the processor executing the BIOS routine by providing to the second computing system unit in response to said received request at least one of said received information stored in said memory before the receipt of said request if any is stored; and

the processor executing the BIOS routine by providing to the second computing system unit at least one of said received information received subsequent to said request.

2.   (Original) The method defined in claim 1 wherein said information comprises at least one of unit information, system information, error information, status information, configuration information, and event information.

3.   (Original) The method defined in claim 1 wherein the receiving said information from the first units is according to an API.

4.   (Original) The method defined in claim 1 wherein the providing said information to the second units is according to an API.

5.   (Original) The method defined in claim 1 wherein the storing the received information in a memory is according to a time of receipt of said information.

6.   (Original) The method defined in claim 1 wherein the providing the received information stored in the memory before a receipt of said request is according to a time of receipt by said processor.

7.   (Original) The method defined in claim 1 wherein said information received by said processor is appended to include at least one of a sequence number and an absolute time.

8.   (Original) The method defined in claim 1 wherein the provided received information stored before the receipt of said request includes one of all said received information and a categorized subset of all said received information.

9.   (Original) The method defined in claim 1 wherein the provided information received subsequent to said request includes one of all said received information and a categorized subset of all said received information

10.   (Previously Presented) A machine-readable medium that provides instructions, which when executed by a processor, cause said processor to perform operations comprising:

during execution of a BIOS routine, receiving information from at least one first computing system units, the information comprises at least one of error information, status information, and configuration information;

during execution of the BIOS routine, storing said received information in a memory;

for each of at least one second computing system units,

during execution of the BIOS routine, receiving an initial request for said received information from the second computing system unit;

during execution of the BIOS routine in response to said request, providing to the second computing system unit at least one of said information stored in said memory before the receipt of said request if any is stored; and

during execution of the BIOS routine, providing to said second computing system unit at least one of said received information received subsequent to said request.

11.   (Original) The medium defined in claim 10 wherein said information comprises at least one of unit information, system information, error information, status information, configuration information, and event information.

12.   (Original) The medium defined in claim 10 wherein the receiving information is according to an API.

13.   (Original) The medium defined in claim 10 wherein the providing said information stored to the second units is according to an API.

14.   (Original) The medium defined in claim 10 wherein the storing the received information in a memory is according to a time of receipt of said information.

15.   (Original) The medium defined in claim 10 wherein the providing the received information stored in the memory before a receipt of said request is according to a time of receipt by said processor.

16.   (Original) The medium defined in claim 10 wherein the information received is appended to include at least one of a sequence number and an absolute time.

17.   (Original) The medium defined in claim 10 wherein the provided information stored before the receipt of said request includes one of all said received information and a categorized subset of all said received information

18.   (Original) The medium defined in claim 10 wherein the provided information received subsequent to said request includes one of all said received information and a categorized subset of all said received information

19.   (Previously Presented) A computer that comprises:

a stored BIOS program in a non-volatile memory that includes instructions that cause said computer to:

receive information from at least one first units coupled to said computer, the information comprises at least one of error information, status information, and configuration information;

store said received information in a memory;

for each of at least one second units coupled to said computer,

receive an initial request for said information from the second unit;

in response to said request, provide to the second unit at least one of said information stored in said memory before the receipt of said request if any is stored, and

provide to the second unit at least one of said received information to the second unit received subsequent to said request.

20.   (Original) The computer defined in claim 19 where the information includes at least one of unit information, system information, error information, status information, configuration information, and event information.

21.   (Original) The computer defined in claim 19 wherein the receive information from the first units is according to an API.

22   (Original) The computer defined in claim 19 wherein the provide to the second unit information is according to an API.

23.   (Original) The computer defined in claim 19 wherein the store the received information in a memory is according to a time or receipt of said information.

24.   (Original) The computer defined in claim 19 wherein the provide the received information stored in the memory before a receipt of said request is according to a time of receipt of said information.

25.   (Original) The computer defined in claim 19 wherein the information received by said processor is appended to include at least one of a sequence number and an absolute time.

26.   (Original) The computer defined in claim 19 wherein the provided information stored before the receipt of said request includes one of all said received information and a categorized subset of all said received information.

27.   (Original) The computer defined in claim 19 wherein the provided information received subsequent to said request includes one of all said received information and a categorized subset of all said received information.